

FACULTY PROFILE

Name of the faculty	ASHWINI GOWDA H B
Designation	Assistant Professor
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Degree	Specialization	University	Year of Passing
M.E	ECE	Bangalore University	2019
B.E	ECE	VTU	2016

Work Experience

Teaching	Research	Industry
0	0	1.8

Publications

National/International Journals	National/International Conferences	Books Authored/Edited
0	1	nil

Area of Interest

VLSI	
Communication	
Logic Design	
Verilog	

Membership in Professional Bodies/University Bodies/Organization

Funded Project

Patent

Publications National/International Journals

Publications

National /International Conferences

- **FPGA Implementation Of Efficient Multiplier Using reversible Logic based Multiplexer**

Faculty Development Programme Participated

Workshop Attended

Awards/Recognition/Achievement/Others