

## FACULTY PROFILE

Name of the faculty	SUMA V SHETTY
Designation	Assistant Professor
Contact Number	9886241437
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Degree	Specialization	University	Year of Passing
M.Tech	VLSI Design & Embedded Systems	VTU	2010
B.E	Electronics & Communication	Mangalore University	1994

## Work Experience

Teaching	Research	Industry
15 Years		8 Years

National/International Journals	National/International Conferences	Books Authored/Edited
8	1	nil

## Area of Interest

VLSI Design and Embedded systems	

## Membership in Professional Bodies/University Bodies/Organization

Nil

Funded Project	KSCST sponsored project "Forest Monitoring System based on GPRS and powered by IoT " selected for Seminar/Exhibition held at NMAM Institute of Technology Nitte on 11 <sup>th</sup> and 12 <sup>th</sup> August 2017.
Patent	nil

## Publications National/International Journals

1. Suma V shetty et.al., " Li-Fi Based High Speed and Secured Data Transfer using MATLAB", International Journal of Progressive Research in Science and Engineering,(IJPRSE), Volume-1, Issue-3, June-2020.
2. Suma V shetty et.al., "Low Wind High Speed Wind Turbine", International Journal of Research in Engineering, Science and Management(IJRESM),ISSN 2581-5792, Volume-3, Issue-5, May-2020
3. Suma V Shetty et.al. , "Li Fi based Data and Audio Communication", International Journal of Engineering Research & Technology,ISSN 2278-0181, volume 8 Issue 5, May 2019
4. Suma V Shetty et.al. , " Library Assistant Robot" , , International Journal of Engineering Research & Technology (IJERT), ISSN 2278-0181, volume 8 Issue 6, May 2019
5. Suma V Shetty, Shilpa V,,Design of Systolic Architecture using Evolutionary Computation, International Journal for Trend in Scientific Research and development,ISSN 2456-6470, vol -2, Issue 4, 2018
6. Suma V Shetty et.al., " Designing a Reconfigurable MPEG Architecture with a lower PSNR ratio with respect to existing JPEG Infrastructure", International Journal of Electrical, Electronics and Computer Systems(IJEECS) ISSN 2347-2820, May 2017.
7. Suma V Shetty , Madhusudhan Patil M "Packet switched Wormhole Router Design & Low Latency Adapter Design for NOC Architecture and its FPGA Implementation", ISSN 2321-0613, Vol 4, Issue 4, June 2016.
8. Design and Implementation of ALU using Redundant Binary Signed Digit, IJCA Proceedings on International Conference on VLSI, Communications and Instrumentation (ICVCI) (5):30–35, 2011

## Publications

### National /International Conferences

1. Suma V Shetty, Madhusudhan Patil M, "Analysis of NoC Adapter and Wormhole Router Design and its Implementation", National Conference on Recent trends in Electronics and Communication, MANTHANA –April ,2016.

## Faculty Development Programme Participated

1. AICTE sponsored FDP on Smart techniques ICT automation and power electronics applications in integration of Renewable energy sources and smart grid, 20th Jan to 1st Feb 2020 at DSCE Bengaluru.
2. AICTE Sponsored Two weeks Faculty Development Program on "Implications of IoT using Android Applications" Dr AIT, Bengaluru from 15/7/2019 to 27/7/2019
3. Internship Program on IoT at Advanced Electronic Systems, Bangalore 25th July 2019
4. Faculty Development Program on "Teacher Empowerment", SCE, 23/7/2018 to 25/7/2018
5. Faculty Development Program, SCE, 22nd January, 2015
6. Faculty Development Program on "Nano Electronics Circuits and Devices", SCE, 10th & 11th December 2012
7. Short Course on Professional Induction Programme, conducted by National Institute of Technical Teachers Training & Research, SCE, 29/1/2008 to 3/2/2008

## Workshop Attended

1. Workshop on "NUVOTON Nu-Micro ARM Cortex and its applications" applications, 18th to 19th March 2014, Dept of Nanotechnology and Dept of Digital Electronics and Communication systems, VTU CPGS, Bangalore.
2. Workshop conducted by Mission 10X, SCE, February 2012
3. Workshop on "Embedded System Design using Microcontrollers" at AIT, January 2010

## Awards/Recognition/Achievement/Others

nil