



SAPTHAGIRI COLLEGE OF ENGINEERING, BENGALURU-560057

Affiliated to VTU, Belagavi & Approved by AICTE, New Delhi

(Accredited by NAAC with "A" grade) (An ISO 9001:2015 and 14001:2015 Certified Institution)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Time-table for the academic year 2020-21 (EVEN Semester)

Semester & Section: IV A/B									
Class Teacher Name: Prof. Preethi T S/Prof. Varshini K									
PERIOD	1	2		3	4		5	6	7
TIME	8:30am	9:30am	10:30am	10:50am	11:50am	12:50 pm	01:45 pm	02:40 pm	03:35 pm
DAY	9:30am	10:30am	10:50am	11:50am	12:50pm	01:45 pm	02:40 pm	03:35 pm	04:30 pm
MON	18EC46	18EC45	SHORT BREAK	18EC42	18EC43	LUNCH BREAK	18EC44	18MAT41	18DIPMAT41
TUE	18EC43	18EC45		18MAT41	18EC42		18ECL47		
WED	18EC44	18EC45		18EC42	18MAT41		18ECL48		
THU	18MAT41	18EC46		18EC44	18EC45		18KAK49	18KAK49	18EC43
FRI	18EC42	18EC43		18EC46	18EC44		18EC42	18EC45	18DIPMAT41
SAT	18EC43	18EC46		18EC44	18KVK49				

Subjects Allocation

Subject Code	Subject Title	Faculty Name	Faculty Code
18MAT41	Complex Analysis, Probability and Statistical Methods	PROF. BHAVYA N P / PROF. BINDU M G	BNP/BMG
18EC42	Analog Circuits	PROF. RAVI SHANKARA M N	RMN
18EC43	Control Systems	PROF. VARSHINI K	VK
18EC44	Engineering Statistics and Linear Algebra	PROF. VINAY H C	VHC
18EC45	Signals and Systems	DR. H C SATEESH KUMAR	HCS
18EC46	8051 Microcontroller	PROF. PREETHI T S	PTS
18ECL47	Microcontroller Laboratory	PROF. SUMA V SHETTY, PROF. PRATHIBHA P, PROF. VARSHINI K, PROF. PADMAVATHI C, PROF. RASHMI B J	
18ECL48	Analog Circuits Laboratory	PROF. RAVISHANKARA M N, DR. PRAKASH JADHAV, PROF. VINAY H C, PROF. GIRISH J R, PROF. RASHMI B J, PROF. VEENA N	
18KAK49/ 18KVK 49	Aadalitha Kannada/ Vyavaharika Kannada	PROF. LAKSHMI	LK

Time-Table Coordinator

HOD

Principal



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Semester & Section: VI A/B									
Class Teacher Name: Prof. Suma C/Prof. Kavitha R J									
PERIOD	1	2		3	4		5	6	7
TIME	8:30am -	9:30am -	10:30am -	10:50am -	11:50am -	12:50 pm -	01:45 pm -	02:40 pm -	03:35 pm -
DAY	9:30am	10:30am	10:50am	11:50am	12:50pm	01:45 pm	02:40 pm	03:35 pm	04:30 pm
MON	18EC643/ 18EC646	18EC62	SHORT BREAK	18EC63	18EC61	LUNCH BREAK	18ECL66		
TUE	18EC62	18EC643/1 8EC646		18EC63	18EC61		18ECL67		
WED	18EC61	18EC62		18EC63	18EC643/ 18EC646		-		
THU	18XX65X	18EC61		18EC643/ 18EC646	18EC62		-		
FRI	18XX65X	18EC61		18EC643/ 18EC646	18EC63		-		
SAT	18XX65X	18EC62		18EC63	-				

Subjects Allocation

Subject Code	Subject Title	Faculty Name	Faculty Code
18EC61	Digital Communication	PROF. SUMA C	SC
18EC62	Embedded Systems	PROF. AGALYA P	AP
18EC63	Microwave and Antennas	PROF. SHOBHA H/PROF. KAVITHA R J	SH/KRJ
18EC643/ 18EC646	Data Structures using C++/Python Application Programming	PROF. GIRISH J R/PROF. VEENA N	GJR/VN
18XX65X	OPEN ELECTIVE-A	-	-
18ECL66	Embedded Systems Lab	PROF. SHOBHA S, DR. SUDHA M S , PROF. PREETHI T S, PROF.SUMA C, PROF.MADHU H M	
18ECL67	Communication Lab	PROF. SHOBHA H, PROF. KAVITHA R J, PROF. GIRISH J R,PROF. VEENA N	

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Time-table for the academic year 2020-21 (EVEN Semester)

Semester & Section: VIII A/B									
Class Teacher Name: Prof. Prathibha P/Prof. Padmavathi C									
PERIOD	1	2		3	4		5	6	7
TIME	8:30am -	9:30am -	10:30am -	10:50am -	11:50am -	12:50 pm -	01:45 pm -	02:40 pm -	03:35 pm -
DAY	9:30am	10:30am	10:50am	11:50am	12:50pm	01:45 pm	02:40 pm	03:35 pm	04:30 pm
MON	17EC82	17EC834	SHORT BREAK	17EC81	17EC82	LUNCH BREAK	PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP		
TUE	17EC81	17EC82		17EC834	17EC81		PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP		
WED	17EC834	17EC82		17EC81	17EC834		PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP		
THU	PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP						PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP		
FRI	PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP						PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP		
SAT	PROJECT WORK/TECHNICAL SEMINAR/INTERNSHIP								

Subjects Allocation

Subject Code	Subject Title	Faculty Name	Faculty Code
17EC81	Wireless Cellular and LTE 4G Broadband	DR. PRAKASH JADHAV/ PROF. PADMAVATHI C	PCJ/PC
17EC82	Fiber Optics and Networks	PROF. PRATHIBHA P/ PROF. SUMA V SHETTY	PP/SVS
17EC834	Machine Learning	PROF. SHOBHA S	SS
17EC84	Internship/ Professional Practice	-	-
17ECP85	Project Work	-	-
17ECS86	Seminar	-	-

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Semester & Section: VI PC									
Class Teacher Name: Prof. Shilpa V									
PERIOD	1	2		3	4		5	6	7
TIME	8:30am	9:30am	10:30am	10:50am	11:50am	12:50 pm	01:45 pm	02:40 pm	03:35 pm
DAY	9:30am	10:30am	10:50am	11:50am	12:50pm	01:45 pm	02:40 pm	03:35 pm	04:30 pm
MON	17EC64	17EC62	SHORT BREAK	17EC61	17EC63	LUNCH BREAK	17ECL67		
TUE	17EC62	17EC61		17EC663	17EC654		17ECL68		
WED	17EC61	17EC62		17EC654	17EC663		-		
THU	17EC63	17EC654		17EC64	17EC62		-		
FRI	17EC663	17EC63		17EC64	17EC654		-		
SAT	17EC61	17EC64		17EC63	17EC663				

Subjects Allocation

Subject Code	Subject Title	Faculty Name	Faculty Code
17EC61	Digital Communication	PROF. SHILPA V	SV
17EC62	ARM Microcontroller and Embedded Systems	PROF. AGALYA P	AP
17EC63	VLSI Design	PROF. PRATHIBHA P	PP
17EC64	Computer Communication Networks	PROF. PADMAVATHI C	PC
17EC654	Digital Switching Systems	PROF. SUMA V SHETTY	SVS
17EC663	Digital System Design using Verilog	DR. SUDHA M S	SMS
17ECL67	Embedded Controller Laboratory	PROF. SHOBHA S, DR. SUDHA M S , PROF. PREETHI T S, PROF.SUMA C, PROF.MADHU H M	
17ECL68	Computer Networks Laboratory	DR. SUDHA M S	

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