

SAPTHAGIRI COLLEGE OF ENGINEERING

Affiliated to VTU, Belagavi & Approved by AICTE, New Delhi
An ISO 9001:2015 and 14001:2015 certified Institution
#14/5, Chikkasandra, Hesaraghatta Main Road, Bengaluru

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Subject	Faculty	Title	Video Link
Network Theory	Dr. H C Sateesh Kumar	Types of sources	https://youtu.be/6jDGjNy1aaU
		Millmans Theorem	https://youtu.be/hbISukIEIvs
		H and T parameters	https://youtu.be/igUHOtt9eHM
		Node Analysis	https://youtu.be/ZyulsD81U-U
Digital system Design	rof. Shilpa V	3-Variable K Map	https://www.youtube.com/watch?v=BjRLtGFBpRc
		5- Variable K-Map	https://www.youtube.com/watch?v=wijJY4q_gwI&t=985s
		Decoder	https://www.youtube.com/watch?v=N-6HX03K0k4
		MUX	https://www.youtube.com/watch?v=l6VK8uVLKBQ
Power Electronics	Prof. Suma v Shetty	Gate characteristics of SCR	https://drive.google.com/file/d/1E1OvaFdmG85tojL7cps_AsicvW5wgez/view?usp=sharing
		Digital Voltmeter	https://drive.google.com/file/d/1E1OvaFdmG85tojL7cps_AsicvW5wgez/view?usp=sharing
Electromagnetic Waves	Prof. Kavitha R J	Electric Field Intensity due to circular ring and DISC	https://youtu.be/psZ48V66L6Y
ITC	Prof. Prathibha P	Cyclic Code - Systematic Form	https://youtu.be/zGJrfgd5S3I
		Encoding Using (n-k) shift register	https://youtu.be/gf5HPh2HaXA
Digital signal Processing	Prof. Suma C	Problems on N-point DFT	https://youtu.be/Qn3EdpgIt6Q
		Problems on DIT-FFT algorithm	https://youtu.be/kHRWPN Pfs
		Problems on FIR filter design	https://youtu.be/x2bS0vOGO-s
		IIR filter design- Bilinear transformation flowchart, Frequency Transformation	https://youtu.be/lcw8kZjWa9U
Operating System	Prof. Veena N	File systems introduction	https://meet.google.com/san-budk-rfy
		Dead Lock	https://meet.google.com/vxe-xhni-hvi
Basic Electronics		Basics of electronics	https://meet.google.com/bfu-bjxq-pnd
Electronic Devices and Instrumentation Lab		Experiment 2c full wave bridge rectifier	https://youtu.be/eQ_vuy0kPW8
		Positive clamper	https://youtu.be/Ok9wNKWZ6nM