

Guest Lecture Report
***Topic: “Introduction to
VLSI in general and GPIO Buffers”***

By

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Date: 28.08.2018

Time: 10.30AM to 12.30 PM

Venue: ALH 406, SCE Bangalore.

The session started with introduction to VLSI Design Methodologies. The speaker provided brief idea about

- Classification in Design Approach: Front End/Back End
- Classification in Design implementation : FPGA, CPLD/Semi custom(Standard cell,ASIC)/Full custom (ASIC , AMS)
- VLSI Design Flow & Supporting Tools

Further speaker elaborated on General Purpose IO Buffers, challenges in designing IO buffer. Why IO buffers are needed in the IC, The IO Form Factors, Types of IO buffers, ESD protection for IO cells. He also discussed about the latch up problem in buffers and how to overcome such issues in the industrial applications.

The session followed by question and answers where students interacted on questions about implementation of projects in the campus. They also discussed about their preparation for getting into VLSI domain in future. The talk was useful and well appreciated by staffs and the students.



Students and faculties attending sessions of Mr.Sagar

Prof. Vani V & Prof. Vani A
Guest Lecture/Workshop/FDP Co-ordinators

Prof. Sandhya Rani MH
HOD ECE