

SAPTHAGIRI COLLEGE OF ENGINEERING

14/5, Chikkasandra, Hesaraghatta Main Road, Bangalore-560057

Department of Computer Science and Engineering

Certificate



Certified that the project work entitled "Automated Timetable Generation Using Genetic Algorithm" carried out by Divya V (1SG13CS041), Kavya P (1SG13CS054), Likhith C (1SG13CS058), Maizna Shariff (1SG13CS060), bonafide students of Saptagiri College of Engineering, in partial fulfillment for the award of Bachelor of Engineering in Computer Science and Engineering of Visvesvaraya Technological University, Belgaum during the academic year 2016-17. It is certified that all corrections/suggestions indicated for Internal Assessment have been incorporated in the report deposited in the department library. The project report has been approved as it satisfies the academic requirements in respect of Project work prescribed for the said degree.

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ABSTRACT

Timetable creation is a very arduous and time consuming task. To create timetable it takes lots of patience and man hours. Timetable is created for various purposes like to organize lecture sessions in educational institutions, timing charts for train and bus schedule and many more. Genetic Algorithm is an algorithm that can be used for optimization. Using Genetic Algorithm, a number of trade-off solutions, in terms of multiple objectives of the problem, could be obtained very easily. By automating this process with the help of computer assistance timetable generator can save a lot of precious time of administrators who are involved in creating and managing various timetables of the institutes. The first phase contains all the compulsory classes of the institute, which are scheduled by a central team. The second phase contains the individual departmental classes.